TITLE: A CAPTURE CLOCK GENERATOR USING MASTER AND SLAVE DLLs
INVENTORS NAME: Feng Lin
DOCKET NO.: 303.734US1

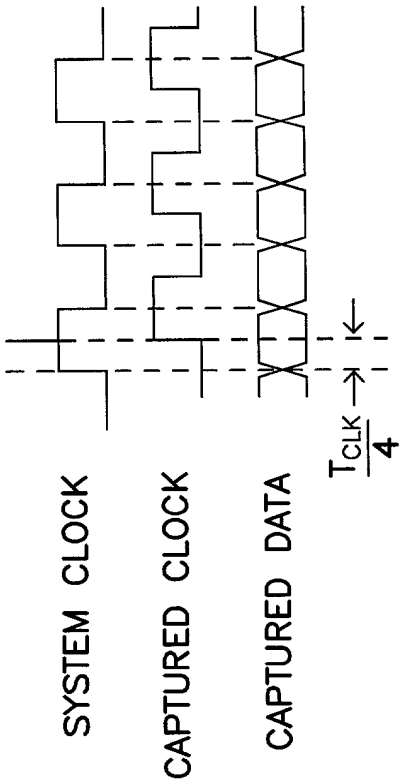

FIG. 1A
PRIOR ART

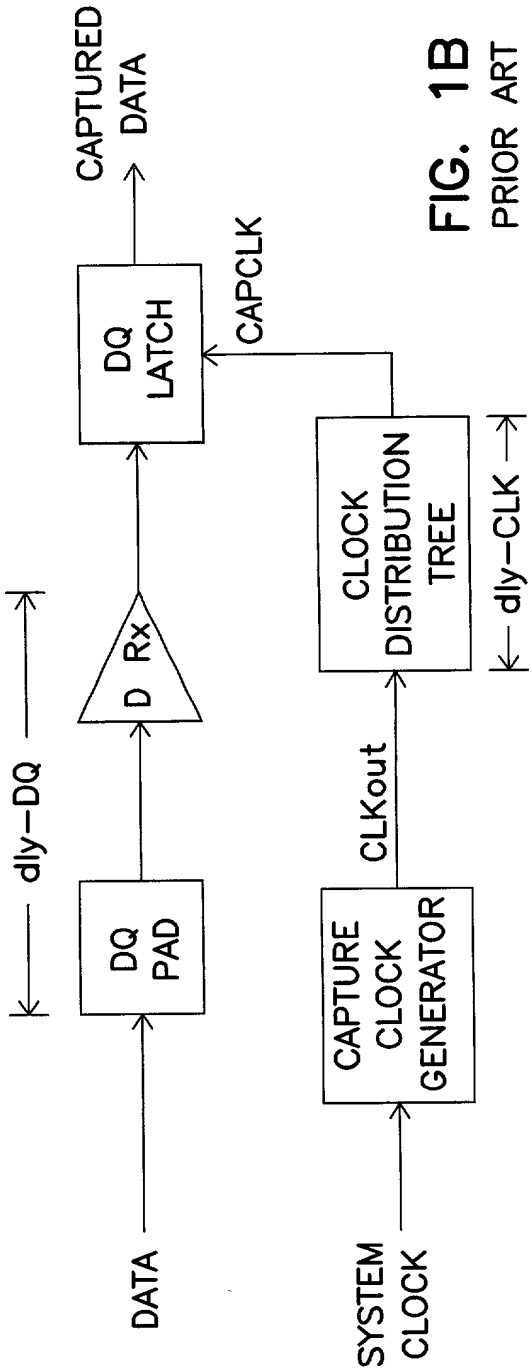

FIG. 1B
PRIOR ART

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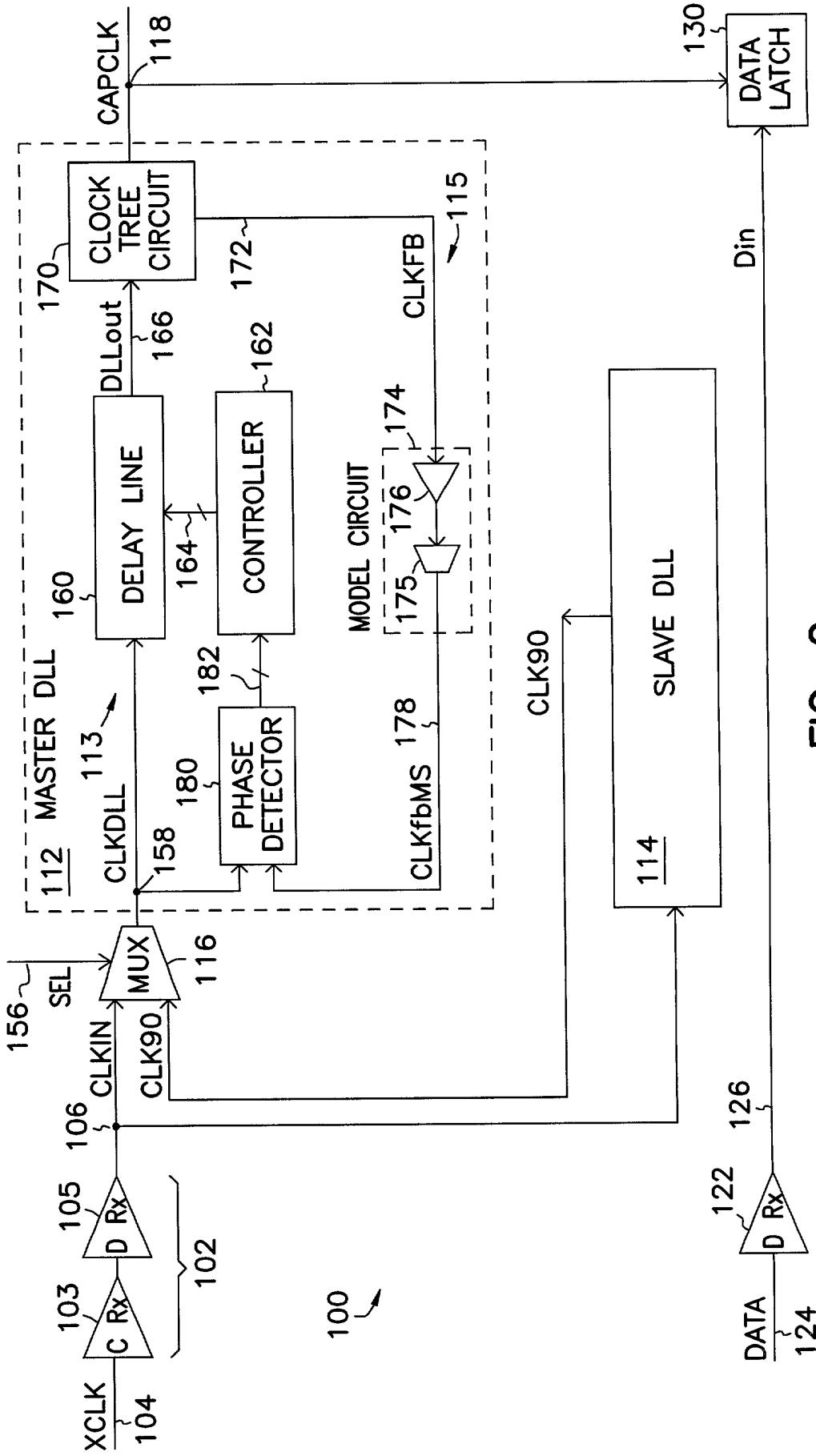

FIG. 2

TITLE: A CAPTURE CLOCK GENERATOR USING MASTER AND SLAVE DLLs
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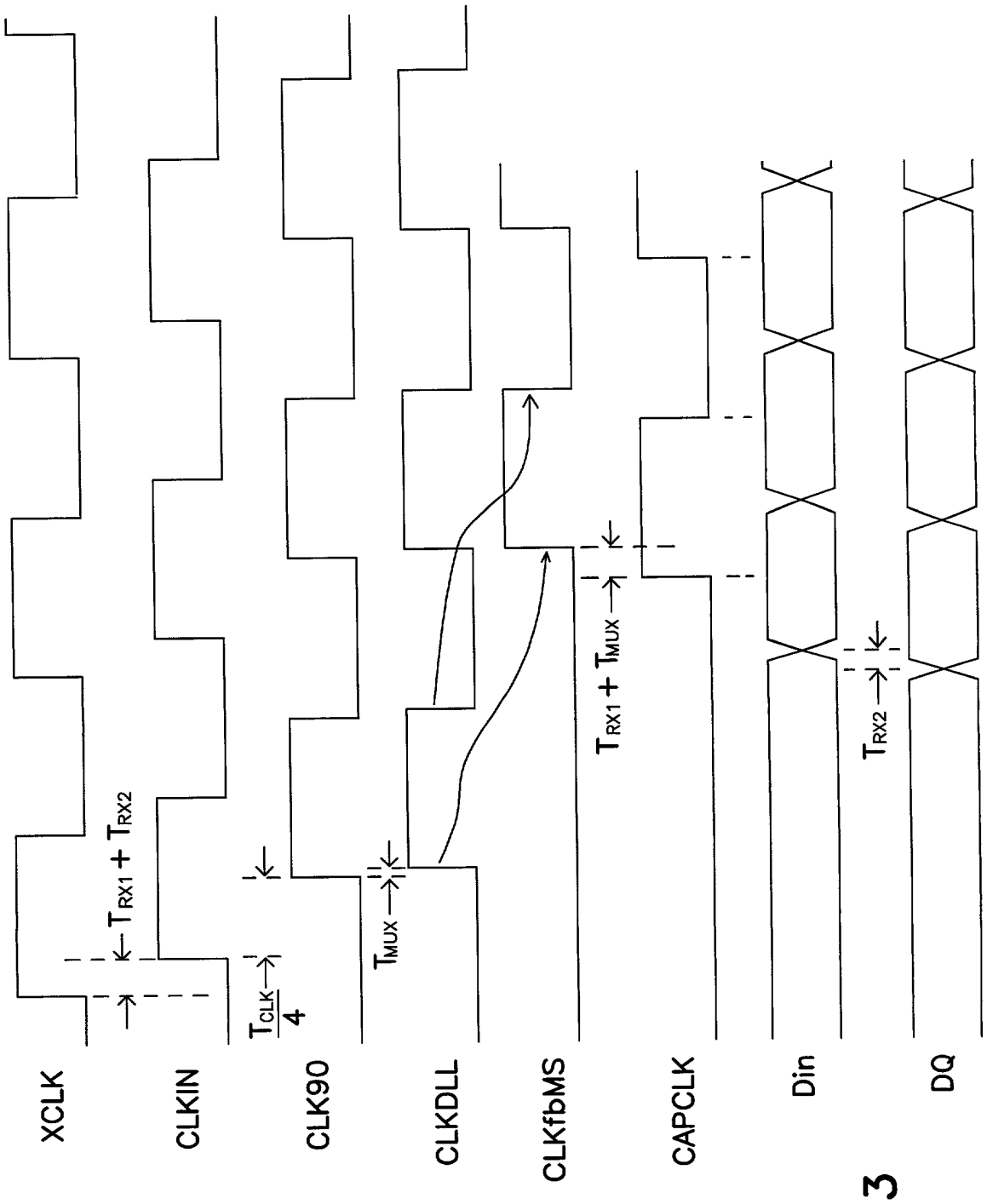

FIG. 3

TITLE: A CAPTURE CLOCK GENERATOR USING MASTER AND SLAVE DLLs
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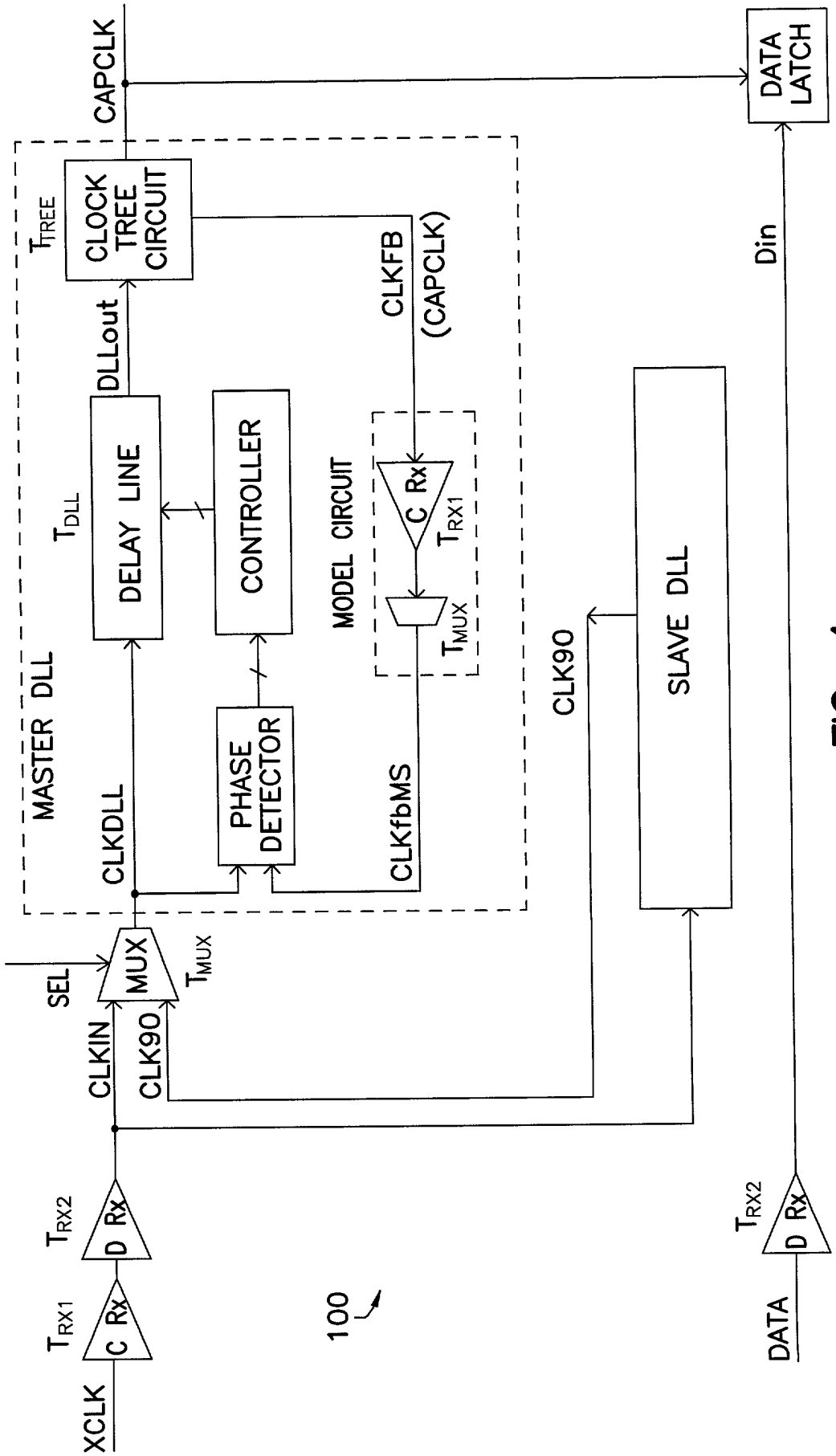

FIG. 4

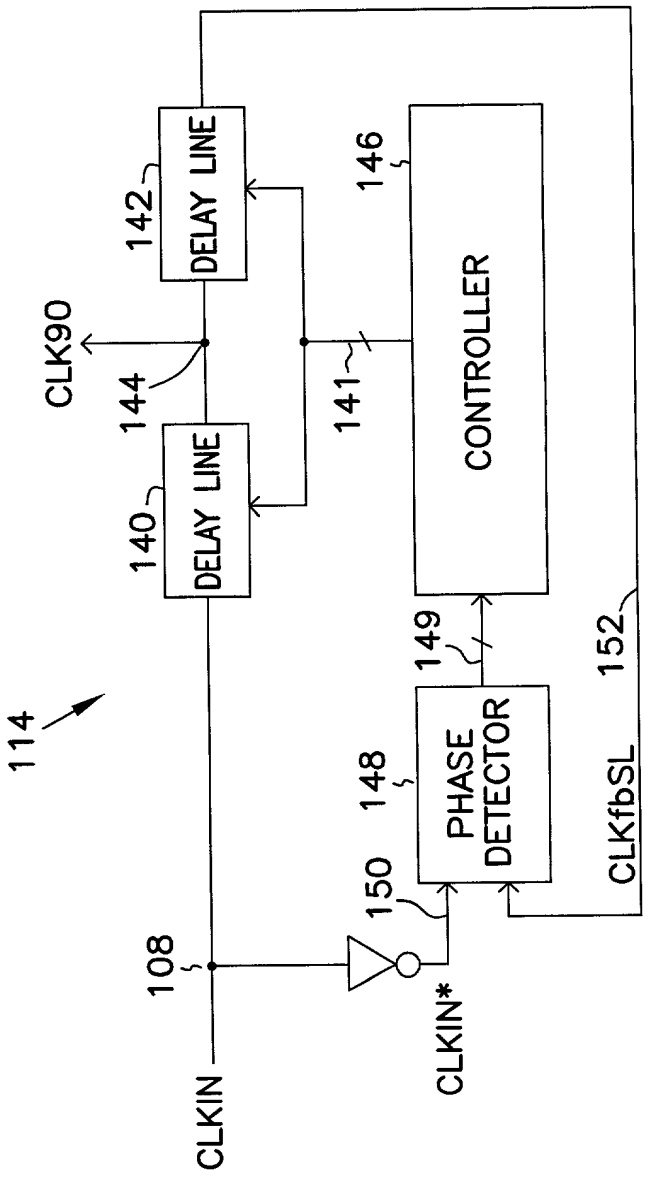
114
CLKIN
108
CLKIN*
150
PHASE DETECTOR
148
149
CONTROLLER
146
141
DELAY LINE
140
DELAY LINE
142
144
CLK90
152
CLKfbSL

FIG. 5

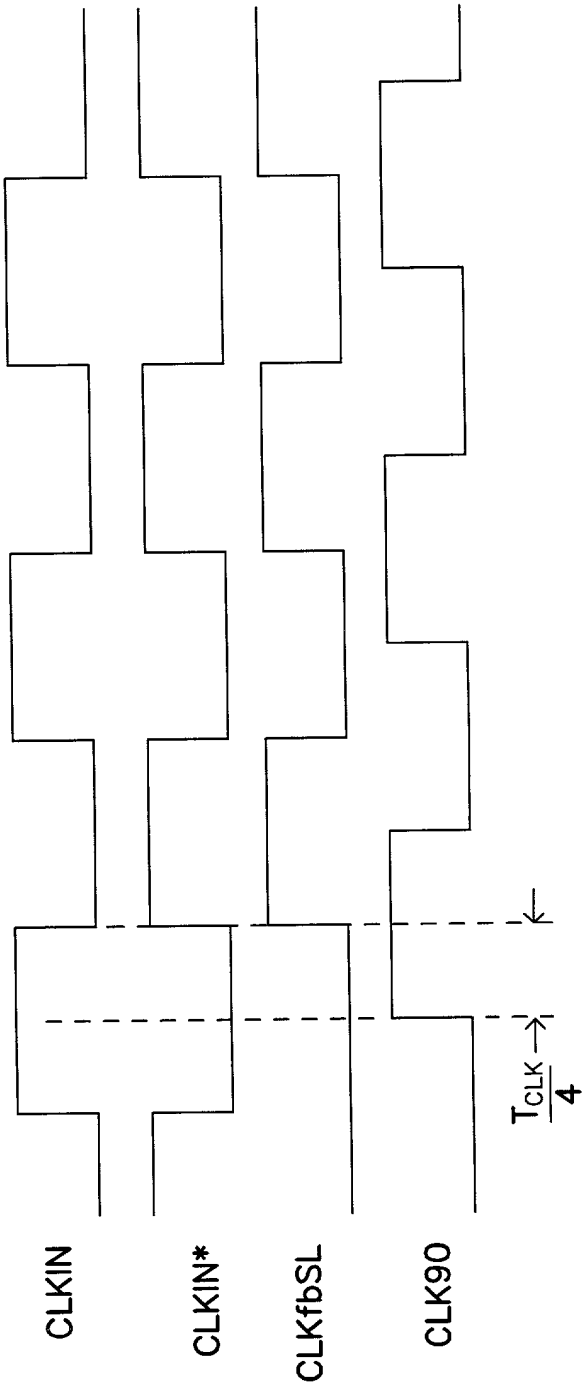

FIG. 6

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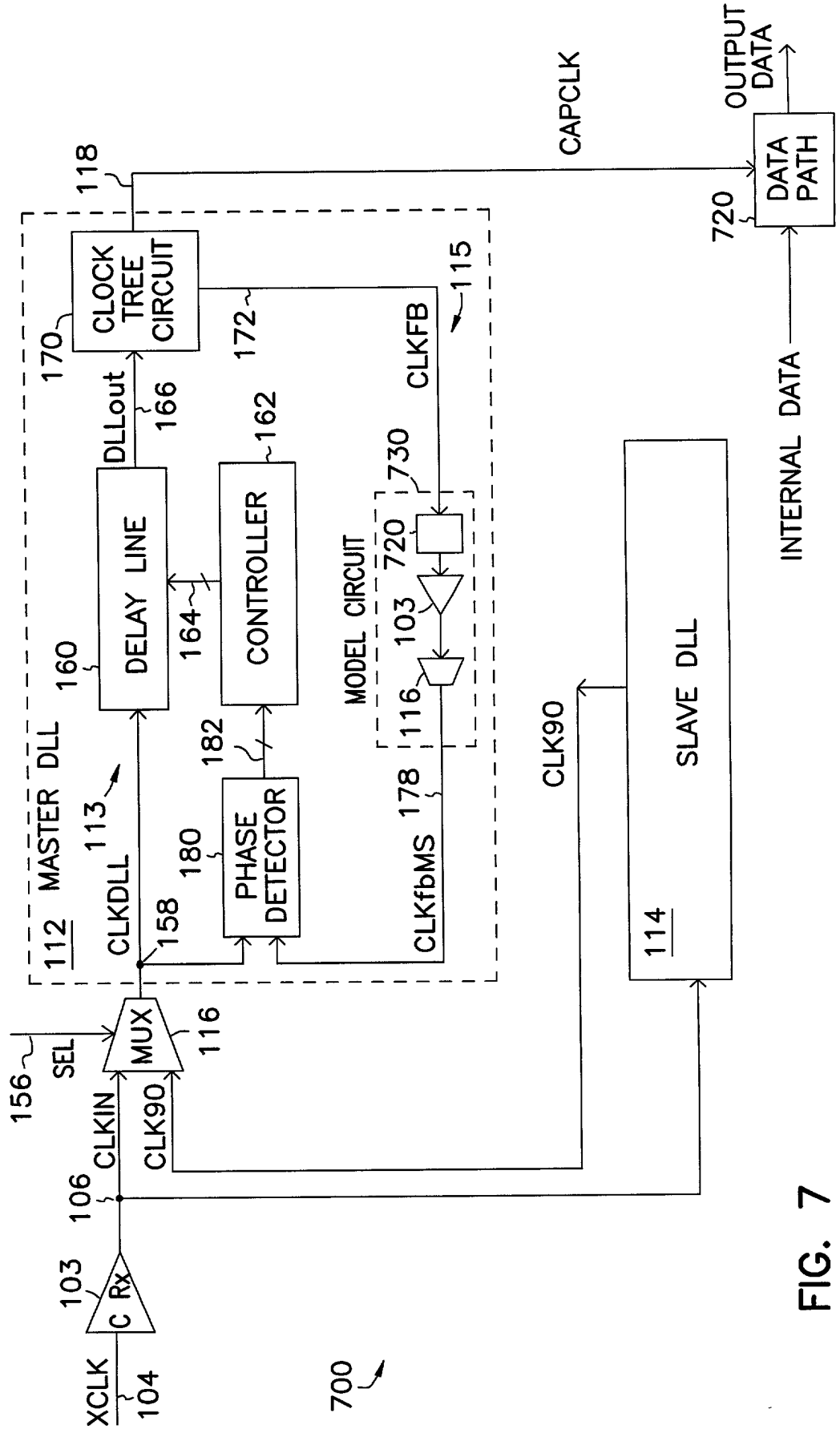

FIG. 7

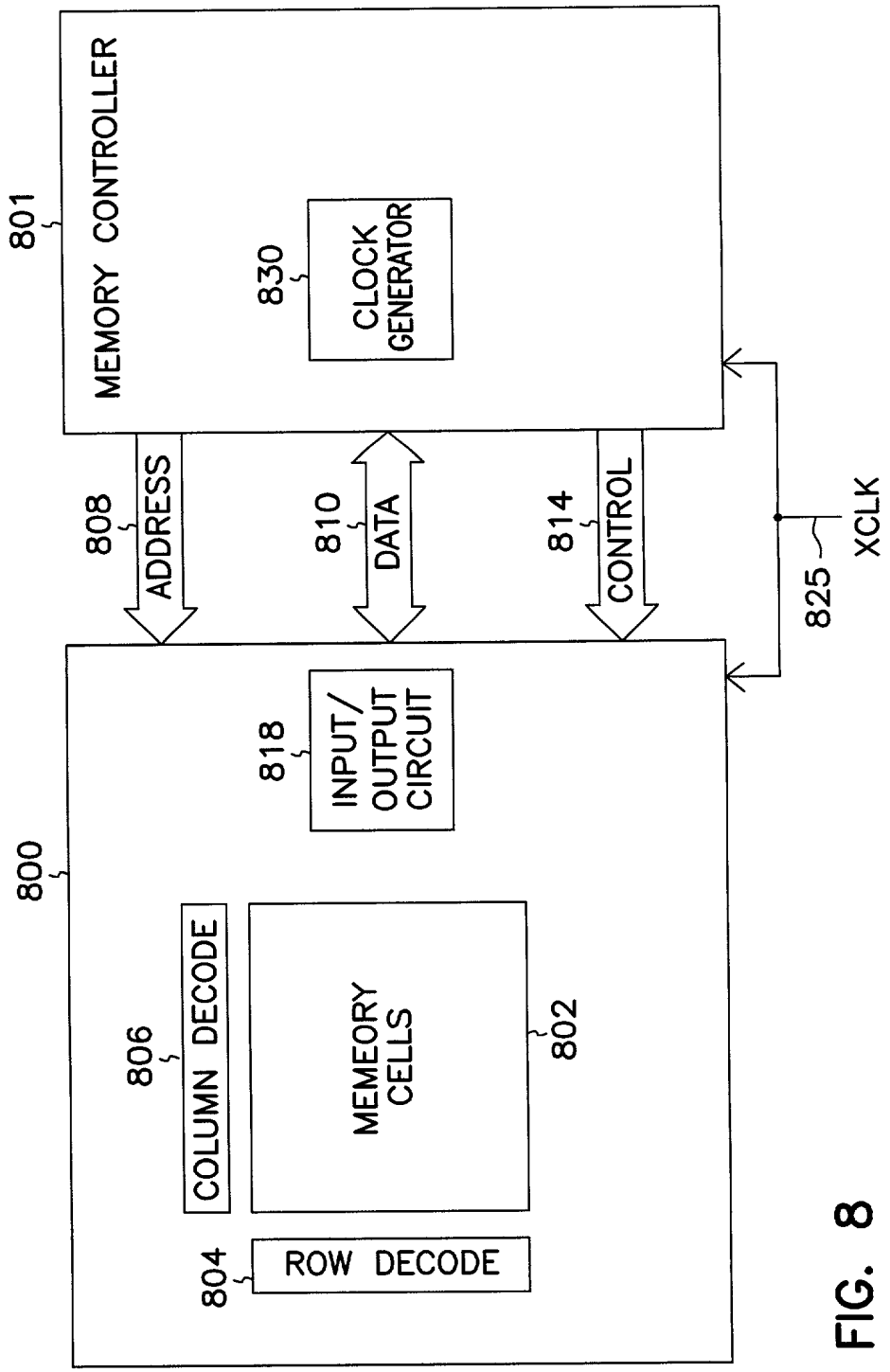
801
MEMORY CONTROLLER
830
CLOCK GENERATOR
808
ADDRESS
810
DATA
814
CONTROL
825
XCLK
800
818
INPUT/ OUTPUT CIRCUIT
806
COLUMN DECODE
MEMEORY CELLS
802
804
ROW DECODE

FIG. 8

TITLE: A CAPTURE CLOCK GENERATOR USING MASTER AND SLAVE DLLs
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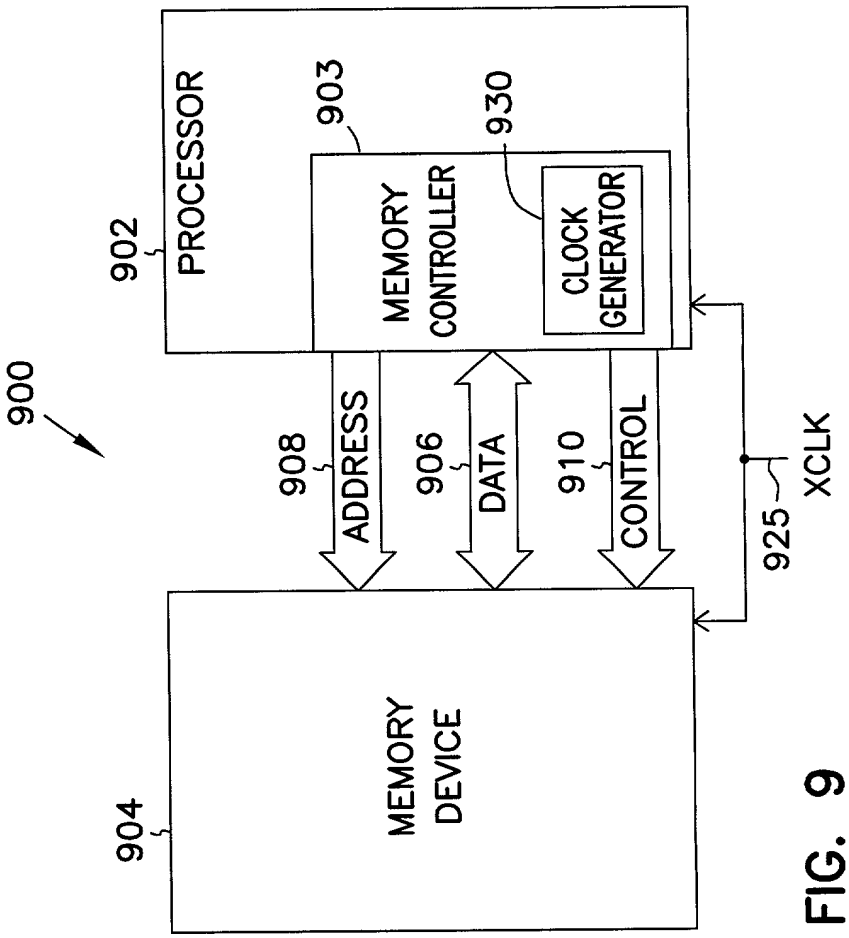

FIG. 9